A Genetic Algorithm to Improve Linux Kernel Performance on Resource-Constrained Devices

James Kukunas  
jkukunas@acm.org

Robert D. Cupper  
bcupper@allegheny.edu

Gregory M. Kapfhammer  
gkapfham@allegheny.edu

Department of Computer Science  
Allegheny College

ABSTRACT

As computers become increasingly mobile, users demand more functionality, longer battery-life, and better performance from mobile devices. In response, chipset fabricators are focusing on elegant architectures to provide solutions that are both low-power and high-performance. Since these architectures rely on unique x86 extensions rather than fast clock speeds and large caches, careful thought must be placed into effective optimization strategies for not only user applications, but also the kernel itself, as the typical default optimizations used by modern compilers do not often take advantage of these specialized features. Focusing on the Intel Diamondville platform, this paper presents a genetic algorithm that evolves the compiler flags needed to build a Linux kernel that exhibits reduced response times.

Categories and Subject Descriptors: D.3.4 [Programming Languages]: Processors-Compilers, optimization

General Terms: Performance

Keywords: Genetic Algorithm, Linux Kernel, Compilers

1. INTRODUCTION

Resource constrained mobile devices, such as netbooks, have become increasingly popular due to their low cost and minimal size and weight. Many motivations exist for optimizing the Linux kernel for netbooks, such as increased battery life, reduced heat generation, and improved kernel performance. Intel offers an optimizing C compiler that is capable of performing architecture-specific optimizations for Intel chipsets, including the Intel Atom processors currently available in most netbooks. The LinuxDNA project patches the Linux kernel to build with the Intel C compiler, thus allowing the kernel to take advantage of these architecture-specific optimizations. Initial results from LinuxDNA found up to a 40% increase in performance within the kernel [4].

Careful consideration must be used when choosing compiler optimizations. While certain optimizations complement other ones and provide further opportunity for performance gains, some hinder the performance of others and thus can lead to kernels that are both larger and slower. The most straightforward approach to solving this problem is to perform exhaustive compilation by building each possible kernel and then selecting the best. However, this is not a feasible approach due to the size of the search space. The system described by this paper examines 107 different compiler flags, which would yield $2^{107}$ different kernels. We use a genetic algorithm (GA) to perform a focused search, thereby reducing the time to find good compiler flags.

Previous work using GAs to evolve compiler flags has seen significant success. Cooper et. al. presented a genetic algorithm to evolve compiler flags to reduce the space overhead of code and achieved up to 40% smaller code size [1]. Davidson et. al. presented a genetic algorithm to evolve optimization orderings within the VPO compiler on the ARM platform, and achieved near-optimal performance [3]. Unlike previous work, this paper focuses on a new and currently unstudied architecture, the Intel Diamondville, and on a different compiler, the Intel C Compiler, and uses response time as an optimization metric rather than code size.

The Intel Diamondville is a micro-architecture designed for netbooks and mobile devices. The Diamondville includes the Intel Atom n270 processor and the 945GME chipset. Operating at a steppable frequency of 1.6 GHz and a front-side bus speed of 533 MHz, the Atom n270 operates significantly more slowly than recent Intel processors. The n270 utilizes an in-order instruction scheduler designed to reduce the footprint, heat generation, and power consumption of the processor. As a result, the n270 is more vulnerable to dependency stalls in the pipeline that are caused by poorly placed instructions. Thus, it becomes the compiler’s job to emulate the pipeline and produce an efficient instruction ordering. Since most x86 processors contain out-of-order instruction schedulers and many compilers do not perform this optimization by default, instruction throughput may suffer when programs are compiled for the n270 [2].

2. GENETIC ALGORITHM

Depending on the compiler flags, processor, and kernel configuration, compiling a Linux kernel can take anywhere from 30 to 60 minutes. When designing the GA, which needs to build a large number of kernels in order to evaluate fitness, performance was a high priority in the design.

Fitness. To properly measure kernel performance the fitness operator only considers the time spent in the kernel. Since user applications interact with the kernel through system calls, timing these operations can adequately character-
By building and sending both the compressed kernel image, the function uses the chosen system calls as microbenchmarks. If the fitness is not available in the lookup table, the compressed kernel image is built, and then kernel compilations. If the fitness is not available in the lookup table, the fitness is represented by a binary string. Each bit in the binary string determines whether a specific compiler option is either on or off. For example, if we had three compiler options, $O_0$, $O_1$, and $O_2$, an individual, say 101, would correspond to using both optimization $O_0$ and $O_2$ together, since bit 0 corresponds to the first optimization and bit 2 corresponds to the third. Binary strings were chosen due to their efficient structure and low overhead, thus enabling each individual to be $\lceil n/8 \rceil$ bytes long when the GA considers $n$ compiler flags.

**Selection, Reproduction, and Mutation.** The selection operator uses truncation to pick the top 75% of the population as candidates for crossover. Returning the population to the specified size, crossover takes the higher bits of the first parent and the lower bits from the second parent and combines them by using a bitwise or operator. With a 10% chance, the mutation operator reads a random byte from /dev/urandom and uses a bitwise or operator to combine it with a random byte from the chosen individual. The GA runs until it completes the specified number of generations.

### Initialization

The initialization phase of the GA focuses on two main goals: parsing the chosen compiler flags and creating the initial population. The compiler options file follows a simple grammar in which each line is either a compiler option followed by a newline or an “ETHER m,” where the next $m$ lines contain compiler options which cannot be used in tandem. After parsing the options, the GA creates a random population where every member is represented by a binary string. Each bit in the binary string determines whether a specific compiler option is either on or off. For example, if we had three compiler options, $O_0$, $O_1$, and $O_2$, an individual, say 101, would correspond to using both optimization $O_0$ and $O_2$ together, since bit 0 corresponds to the first optimization and bit 2 corresponds to the third. Binary strings were chosen due to their efficient structure and low overhead, thus enabling each individual to be $\lceil n/8 \rceil$ bytes long when the GA considers $n$ compiler flags.

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### 3. PRELIMINARY RESULTS

The genetic algorithm was run with a population size of 10 for 5 generations. As shown in Table 1, we compared the performance of the evolved kernel with the default Fedora kernel by measuring the time overhead of three frequently invoked system calls not used in the GA’s fitness function. The kernel resulting from the genetic algorithm performed approximately 17% to 30% better than the default Fedora kernel. This result stems from the fact that the GA selected compiler flags such as -axeSSE3, which utilizes SSE instructions, -vec, which vectorizes loops, and -vec-guard-write, which avoids unnecessary stores during vectorized loops. It is anticipated that the profiling and inclusion of additional system calls and increasing both the population size and the number of generations will confirm these early results. We intend to conduct further experiments in order to study how well a wide variety of GA configurations produce Linux kernels that improve user-perceived performance for commonly executed netbook-based software applications.

### 4. REFERENCES


### Table 1: System Call Performance Results.

<table>
<thead>
<tr>
<th>Method</th>
<th>Fork (ns)</th>
<th>Mmap (ns)</th>
<th>Socket (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fedora Default</td>
<td>254212</td>
<td>35552</td>
<td>31187</td>
</tr>
<tr>
<td>Evolved Kernel</td>
<td>191953</td>
<td>25048</td>
<td>25959</td>
</tr>
<tr>
<td>% Reduction</td>
<td>24.49%</td>
<td>29.91%</td>
<td>16.79%</td>
</tr>
</tbody>
</table>

**Figure 1** Profile Data Used in Fitness Evaluation.

Only 33/330=10% of System Calls Have Non-Zero Counts

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